

**REMARKS**

Claims 1-16 are pending in this application. By this Amendment, claims 1, 10-12, 15 and 16 are amended. Reconsideration of the application is respectfully requested.

The Office Action rejects claims 1-16 under 35 U.S.C. §103(a) over Yamazaki et al. (U.S. Patent No. 5,349,366) ('366) in view of Yamazaki (U.S. Patent No. 6,545,656) ('656). The rejection is respectfully traversed.

In particular, none of the applied references, alone or in combination, disclose or suggest a driving method and associated electro-optical device that includes a setting step of supplying a first on-signal to a switching transistor and of supplying a set signal to select a conducting state or a non-conducting state of the driving transistor to the driving transistor via the data line and the switching transistor in accordance with a period for which the first on-signal is supplied, wherein the period for which the first on-signal is supplied coincides with a period for which the set signal is supplied, as recited in independent claims 1, 10-12, 15 and 16. Support for this feature can be found in the specification at, for example, page 9, line 34 – page 10, line 1.

Specifically, '656 teaches a liquid crystal display device with no image persistence that includes a pixel portion in which a plurality of pixel TFTs are matrix-wise disposed, a source driver and a gate driver which feed a plurality of the TFTs with picture signals, and a liquid crystal material which has substantially no threshold value (Abstract).

Moreover, '366 teaches an electro-optical display device operating in an active matrix mode that includes pixels each provided with a first element for selecting the pixel, the second element for supplying electric current to the pixel in accordance with the information transferred from the first element, and a memory element which stores the signal having output from the first element (Abstract).

However, neither '366 nor '656, alone or in combination, disclose or suggest that the period for which the first off signal is supplied coincides with the period for which the set signal is supplied.

'366 teaches supplying a first on-signal to the switching transistor (Fig. 1B,  $V_g$  at  $t_0$ ) and supplying a set signal to select a conducting state or non-conducting state (Fig. 1B, item  $V_d$  at  $t_0$ ). However, '366 does not disclose or suggest that the period for which the first on-signal is supplied coincides with a period for which the set signal is supplied, as recited in the independent claims.

In fact, a closer examination of Fig. 1B indicates that the set signal and the first on signal, as identified by the Office Action, do not coincide with each other, as recited in the independent claims. Moreover, '656 fails to cure deficiencies in '366 in disclosing or rendering obvious a driving method for an electro-optical device and an associated optical device wherein a period for which the first on-signal is supplied coincides with a period for which the set signal is supplied, as recited in independent claims 1, 10-12, 15 and 16. Thus, independent claims 1, 10-12, 15 and 16, and their dependent claims, are patentable over the applied references. Accordingly, withdrawal of the rejection of the claims under 35 U.S.C. §103(a) is respectfully requested.

In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance of claims 1-16 are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfully submitted,



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